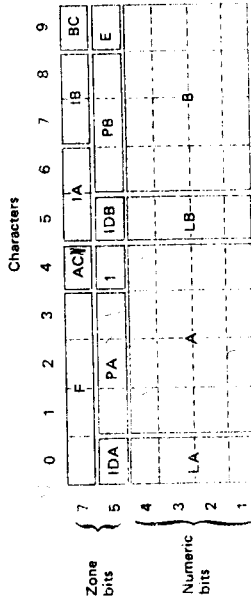


Instruction bit layout



Each of the bit fields shown in the above illustration is used by the ACU in interpreting and executing instructions.

| Field | Size in bits | Interpretation and use |
|-------|--------------|---|
| F | 4 | Operation code of the instruction |
| AC/BC | 1 | A/B address common/partition indicator where 0 = partition and 1 = common |
| IA/IB | 2 | A/B address indexing field |

| Bit content | Meaning |
|-------------|-------------------------------------|
| 00 | No indexing |
| 01 | Indexing using index register one |
| 10 | Indexing using index register two |
| 11 | Indexing using index register three |

IDA/IDB 1 Indirect addressing indicator for A/B address where 0 = indirect and 1 = direct

PA/PB 3 Page indicator for A/B address

| Page | Bit settings | Page | Bit settings |
|------|--------------|------|--------------|
| 0 | 111 | 4 | 110 |
| 1 | 011 | 5 | 010 |
| 2 | 101 | 6 | 100 |
| 3 | 001 | 7 | 000 |

E 1 Extended indexing bit

where 0 = full indexing and 1 = page indexing
page indexing only for Model 21

LA/LB 4 Length specifier for A/B address

A/B 16 A/B address within page

Reserved, must be set to 1

Disc-address bit layout

R is read-after-write bit. (When using an MDC II Disc Controller, 0 indicates suppression of automatic read-after-write checking.)

D is drive number

A is arm number. Disregarded by Model 43, (but bit 2 is used to extend the H field.)

H is hundreds digit of a three-digit track number

T is tens and units digits of a three-digit track number

S is tens and units digits of a two-digit sector number

